Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Cancelled).

2. (Previously Presented) The method of claim 3 wherein prior to producing the secret value, the method further comprises:

performing the periodic event; and

generating the short term value.

3. (Currently Amended) A method comprising:

receiving a first command from a second device by a first device, the first command being generated only once upon an initial power-up sequence by the second device;

within a first device, in response to the first command, generating data for permanent storage in a protected area of internal memory of the first device that prevents subsequent modification of the data; and

within the first device, producing a secret value being a combination of both (1) the data and (2) a short term value generated in response to a periodic event, the periodic event being a power-up sequence by a platform employing the first device.

4. (Currently Amended) The method of claim 3, wherein the second device is a chipset and the first device is an integrated circuit component including a processor, an internal memory within a protective package. ~~prior to generating the data, the method further comprises:~~

~~transmitting a first command from a second device to the first device.~~

5. (Currently Amended) The method of claim 3[[4]], wherein prior to producing the secret value, the method further comprises:

transmitting the data to the second device.

6. (Currently Amended) The method of claim 5, wherein prior to producing the secret value, the method further comprises:

~~transmitting~~ receiving a second command from the second device ~~to~~ by the first device; and

generating the short term value internally within the first device in response to the second command.

7. (Original) The method of claim 6, wherein prior to or concurrently with producing the secret value, the method further comprises:

transmitting the short term value to the second device.

8. (Previously Presented) The method of claim 3, wherein the combination is a result produced by successively performing a hash operation on both the data and the short term value.

9. (Currently Amended) A method comprising:

generating a long term value within a first device, the long term value generated upon detecting an initial power-up sequence and receipt of information from a second device;

permanently storing the long term value within a protected area of an internal memory of the first device;

providing the long term value to the second device communicatively coupled to the first device;

generating a short term value within the first device, the short term value is modified after each power cycle;

providing the short term value to the second device;

generating a secret value within the first device after each power cycle, the secret value being a combination of both the long term value and the short term value; and

generating the secret value within the second device based on the long term value and the short term value.

10. (Cancelled).

11. (Previously Presented) The method of claim 9, wherein prior to generating the long term value, the method further comprises:

transmitting a first command from the second device being an input/output control hub (ICH) to the first device being a trusted platform module (TPM).

12. (Currently Amended) The method of claim 9, wherein the long term value is generated in response to the ~~an~~ initial power-up sequence when the first device is in communication with the second device.

13. (Original) The method of claim 12, wherein prior generating the short term value, the method further comprises:

transmitting a second command from the second device to the first device.

14. (Original) The method of claim 9, wherein the combination is a result produced by successively performing a hashing operation on both the data and the short term value.

15. (Currently Amended) A platform comprising:

a link;

an input/output control hub (ICH) coupled to the link; and

a trusted platform module (TPM) coupled to the link, the TPM including

a package,

an asymmetric key generation unit contained within the package, the asymmetric key generation unit to generate a long term value and a short term value, the long term value generated upon detecting an initial power-up sequence based on information provided by the ICH, and

an internal memory contained within the package, the internal memory to permanently store the long term value and to temporarily store the short term value and a secret value being a combination of the long term value and the short term value.

16. (Original) The platform of claim 15, wherein the ICH including an internal memory.

17. (Original) The platform of 16, wherein the TPM transmits the long term value to the ICH over the link during manufacture of the platform and transmits the short term value to the ICH over the link in response to a power-up sequence by the platform.

18. (Original) The platform of claim 15, wherein the asymmetric key generation unit of the TPM includes a number generator.

19. (Original) The platform of claim 15, wherein the TPM further comprises a cryptographic engine performing a successive hashing operation on both the long term value and the short term value to produce the secret value.

20. (Currently Amended) A device comprising:

an internal memory; and

an asymmetric key generation unit to generate, in response to an initial non-repeating event, a unique long term value for permanent storage in a protected area of the internal memory and to generate, in response to a periodic event, a short term value for storage in the internal memory; and

a cryptographic engine to produce a secret value by combining both the long term value and the short term value.

21. (Original) The device of claim 20, wherein the periodic event includes a power-up sequence by a platform employing the device.

22. (Original) The device of claim 20, wherein the initial event includes an initial power-up sequence of the device when in communication with another device.

23. (Original) The device of claim 20, wherein the internal memory includes a non-volatile memory and a volatile memory.

24. (Original) The device of claim 20, wherein the cryptographic engine performs successive hashing operations on the long term value and the short term value when combining the long term value and the short term value.

25. (Currently Amended) A program loaded into platform readable memory for execution by a first device of a platform, the program comprising:

code to generate data for permanent storage in a protected area of internal memory of the first device in response to an initial non-repeating event; and

code to produce a secret value being a combination of both the data and a short term value that is generated in response to a periodic event.

26. (Original) The program of claim 25 further comprising:

code to generate the short term value in response to a periodic event.

27. (Original) The program of claim 25, wherein the periodic event includes a power-up sequence by the platform.

28. (Previously Presented) The program of claim 25, wherein the initial event is a first power-up sequence performed during assembly of the platform.